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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/702,095
	Filing Date	November 5, 2003
	First Named Inventor	William B. Dress
	Art Unit	2871
	Examiner Name	Not yet assigned
Total Number of Pages in This Submission	Attorney Docket Number	2503702-991111

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Group
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Or Individual name	EDWARD B. WELLER (Reg. No. 37,468) Gray Cary Ware & Freidenrich LLP
Signature	<i>Ed B. Weller</i>
Date	August 11, 2004

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.			
Typed or printed name	Susan Pigue		
Signature	<i>Susan Pigue</i>	Date	August 11, 2004

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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INFORMATION DISCLOSURE STATEMENT	Applicant	Dress, et al.
	Application No.	10/702,095
	Filed:	November 5, 2003
	For:	Integrated Circuit Optical Signal Emitters
	Group Art Unit:	2871
	Examiner:	Not yet assigned
	Attorney Docket No.:	2503702-991111

Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

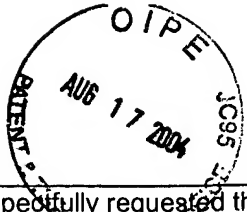
In accordance with the provisions of 37 C.F.R. § 1.56(a) and 37 C.F.R. § 1.97, Applicant(s) hereby make of record the references listed on the accompanying Form PTO-1449 for consideration by the Examiner in connection with the examination of the above-identified patent application.

This Information Disclosure Statement:

(a)	<input type="checkbox"/>	accompanies a new patent application submitted herewith.
(b)	<input checked="" type="checkbox"/>	is filed within three (3) months of the Filing Date or before the mailing date of a First Office Action on the merits; OR
(c)	<input type="checkbox"/>	after the period defined in (b) but before the mailing date of a Final Rejection or Notice of Allowance, OR
(d)	<input type="checkbox"/>	is filed after the first Office Action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned prior to the mailing date of either a final rejection or a notice of allowance, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below OR
(e)	<input type="checkbox"/>	is filed after the mailing date of either a final rejection or a notice of allowance, and the issue fee has not been paid, and is accompanied by the requisite petition fee (\$130) set forth in 37 CFR § 1.17(l)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

As required under § 1.97(e), Applicants, through the undersigned, hereby state either that [check the appropriate space]:

(f)	<input checked="" type="checkbox"/>	Each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of the Information Disclosure Statement; or
(g)	<input type="checkbox"/>	No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.



It is respectfully requested that each of the references shown on the attached Form PTO-1449 be made of record in this application. A copy of the PCT Search Report and the cited references are enclosed.

The Commissioner is authorized to charge any deficiencies and credit any overpayment of fees to our Deposit Account No. 07-1896.

Respectfully submitted,

Date: August 11, 2004

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By: Ed B. Weller

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Form PTO-1449 (modified 2/91) INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	U.S. DEPT. OF COMMERCE Patent and Trademark Office		Attorney Docket Number: 2503702-991111	Serial Number: 10/702,095
	Applicants: Dress, et al.			
	Filing date: November 5, 2003		Group art unit: 2871	

U.S. PATENT DOCUMENTS

Examiner Initial	Patent number	Date	Name	Class	Sub- class	Filing date if appropriate

FOREIGN PATENT DOCUMENTS

	Document number	Date	Country	Class	Sub- class	Translation	
						YES	NO
						Abstract	
						Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	PCT Search Report mailed on May 13, 2004 corresponding to PCT Application No. US03/35242.
	McCormick et al., "Five-Stage Free-Space Optical Switching Network With Field-Effect Transistor Self-Electro-Optic-Effect-Device Smart-Pixel Arrays," March 10, 1994, pp 1601-1618, Applied Optics.
	Cloonan, et al., "Shuffle-Equivalent Interconnection Topologies Based On Computer-Generated Binary-Phase Gratings," March 10, 1994, pp. 1405-1430, Applied Optics.

Examiner:	Date Considered:
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP '609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	